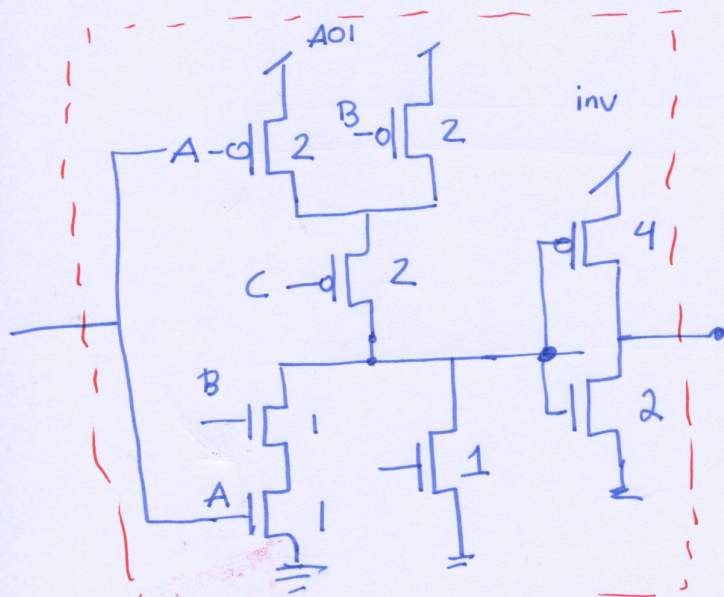


Regarding problem 6 g & p for AO gate

How to derive p & g for AO gate



AO gate

inverter:

$$g_{inv} = 1$$

$$p_{inv} = p_{inv}$$

$$g_{AOI} = \frac{3RC}{\frac{3}{2} \cdot RC} = \frac{6}{3} = 2$$

$$P_{AOI} = \frac{4 \cdot RC}{\frac{3}{2} RC} \cdot p_{inv} = \frac{8}{3} p_{inv}$$

We should now view the two cells together as one cell

Step one: write down the normalized delay for entire

$$AO \text{ gate } d_{AO} = P_{AOI} + g_{AOI} \cdot h_{AOI} + p_{inv} + g_{inv} \cdot h_{inv}$$

constant part = P_{AO}

LOAD dependent part
should give
 $g_{AO} \cdot h_{AO}$

constant part: $P_{AO} = \frac{8}{3} p_{inv} + 2 \cdot \frac{6C}{3C} + p_{inv} =$
does not depend
on C_{LOAD}

$$\frac{13}{2} \cdot p_{inv} + 4$$

load dependent part: $g_{inv} \cdot h_{inv} = g_{inv} \cdot \frac{C_{LOAD}}{C_{IN, INV}}$

but for the AO gate we have $h_{AO} = \frac{C_{LOAD}}{C_{IN, AO}}$

so we use $g_{inv} \cdot \frac{C_{LOAD}}{C_{IN, INV}} = \left[g_{inv} \cdot \frac{C_{IN, AO}}{C_{IN, INV}} \right] \cdot \left[\frac{C_{LOAD}}{C_{IN, AO}} \right]$
gives $g_{AO} \equiv h_{AO}$

model for AO gate $\stackrel{P_{AOI}}{\approx} \frac{13}{2} \cdot p_{inv} + 4 \approx 8$

$$g_{AO} = \frac{3C}{6C} = \frac{1}{2}$$

Note! In such a case it is possible to have $g < 1$!