

A

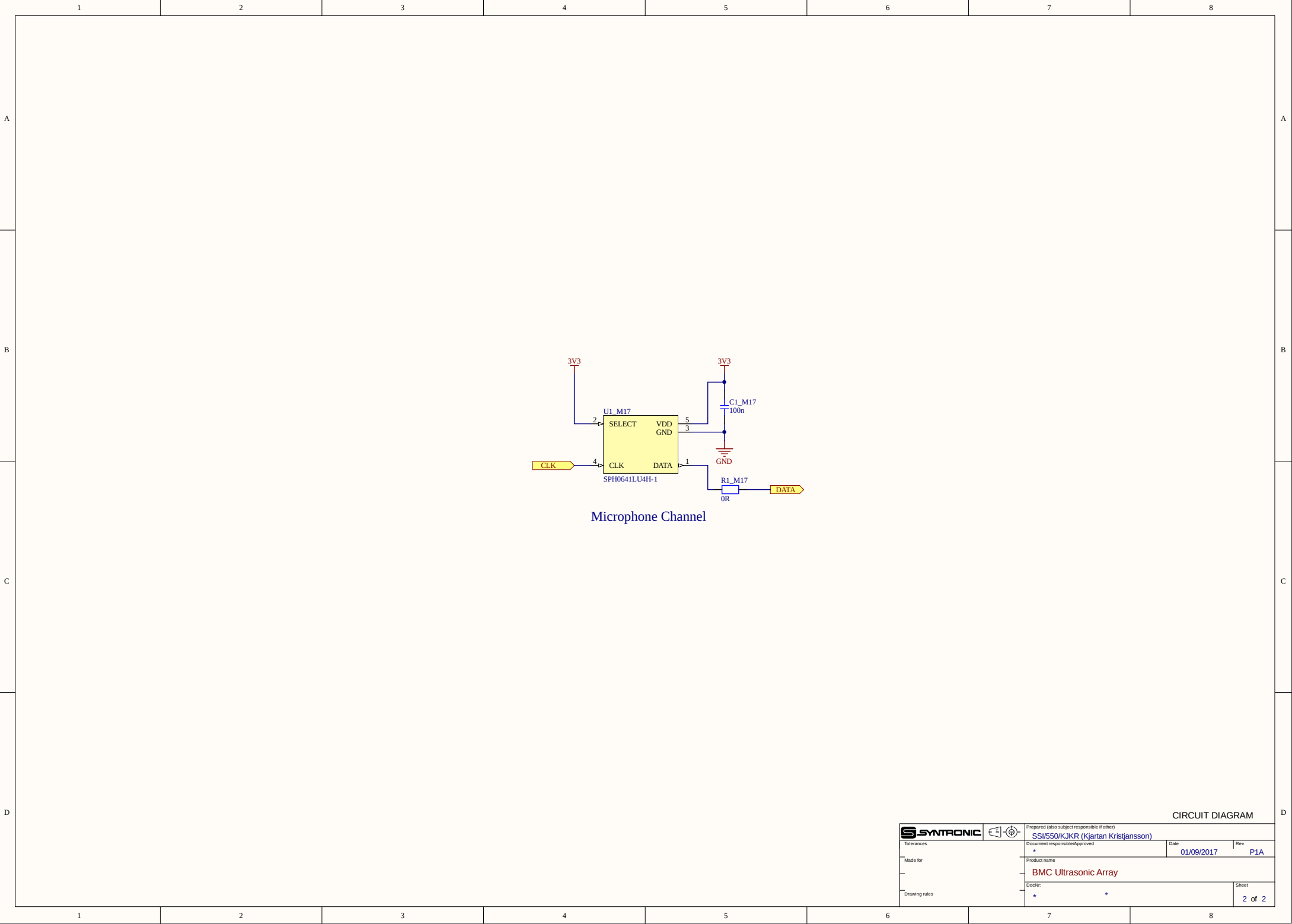


C

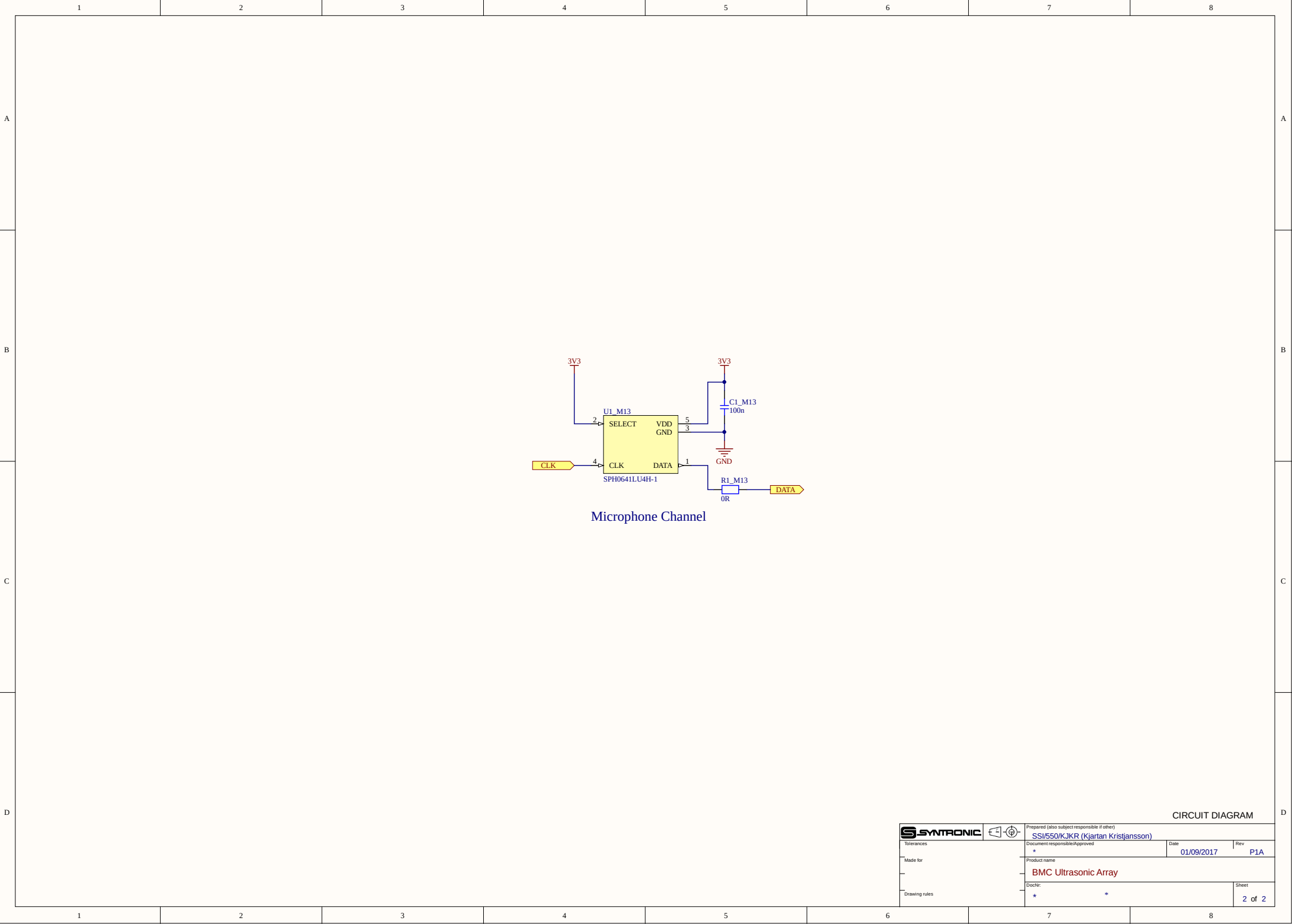
D



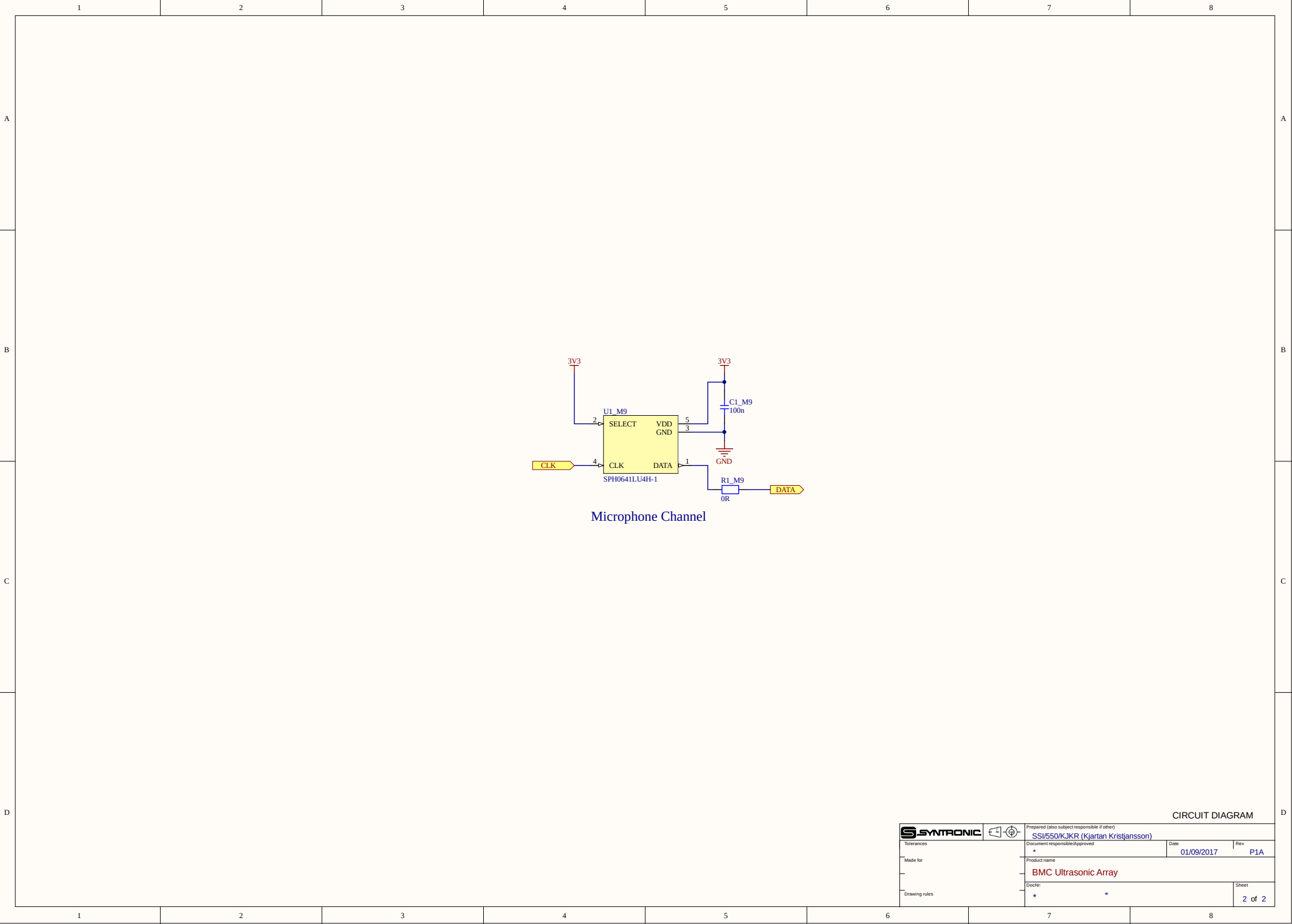
 	Prepared (also subject responsible if other)		
	<u>SSI/550/KJKR (Kjartan Kristjansson)</u>		
	Tolerances	Document responsible/Approved	Date
	Made for		01/09/2017
	Product name	P1A	
	BMC Ultrasonic Array		
Drawing rules	Dtch:	Sheet	
			2 of 2



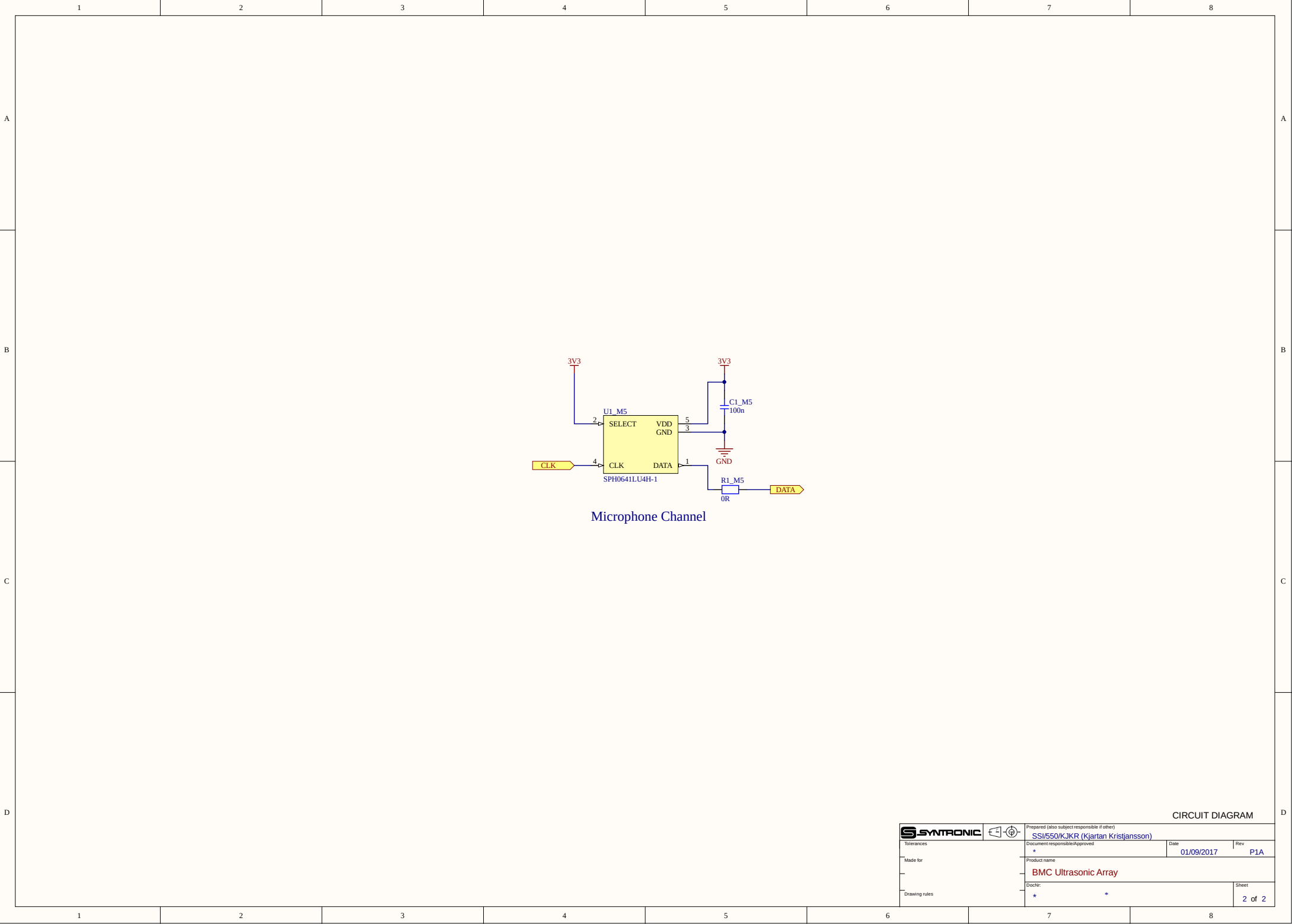
CIRCUIT DIAGRAM				
 	Prepared (also subject responsible if other) SSI/550/KJKR (Kjartan Kristjansson)			
	Tolerances	Document responsible/Approved	Date	Rev
	Made for	Product name	01/09/2017	P1A
	Drawing rules	Drachler	Sheet 2 of 2	



CIRCUIT DIAGRAM				
 	Prepared (also subject responsible if other) SSI/550/KJKR (Kjartan Kristjansson)			
	Document responsible/Approved		Date	Rev
	*		01/09/2017	P1A
	Product name BMC Ultrasonic Array			
Tolerances	Drafter:			Sheet
	*			2 of 2
Made for				
Drawing rules				



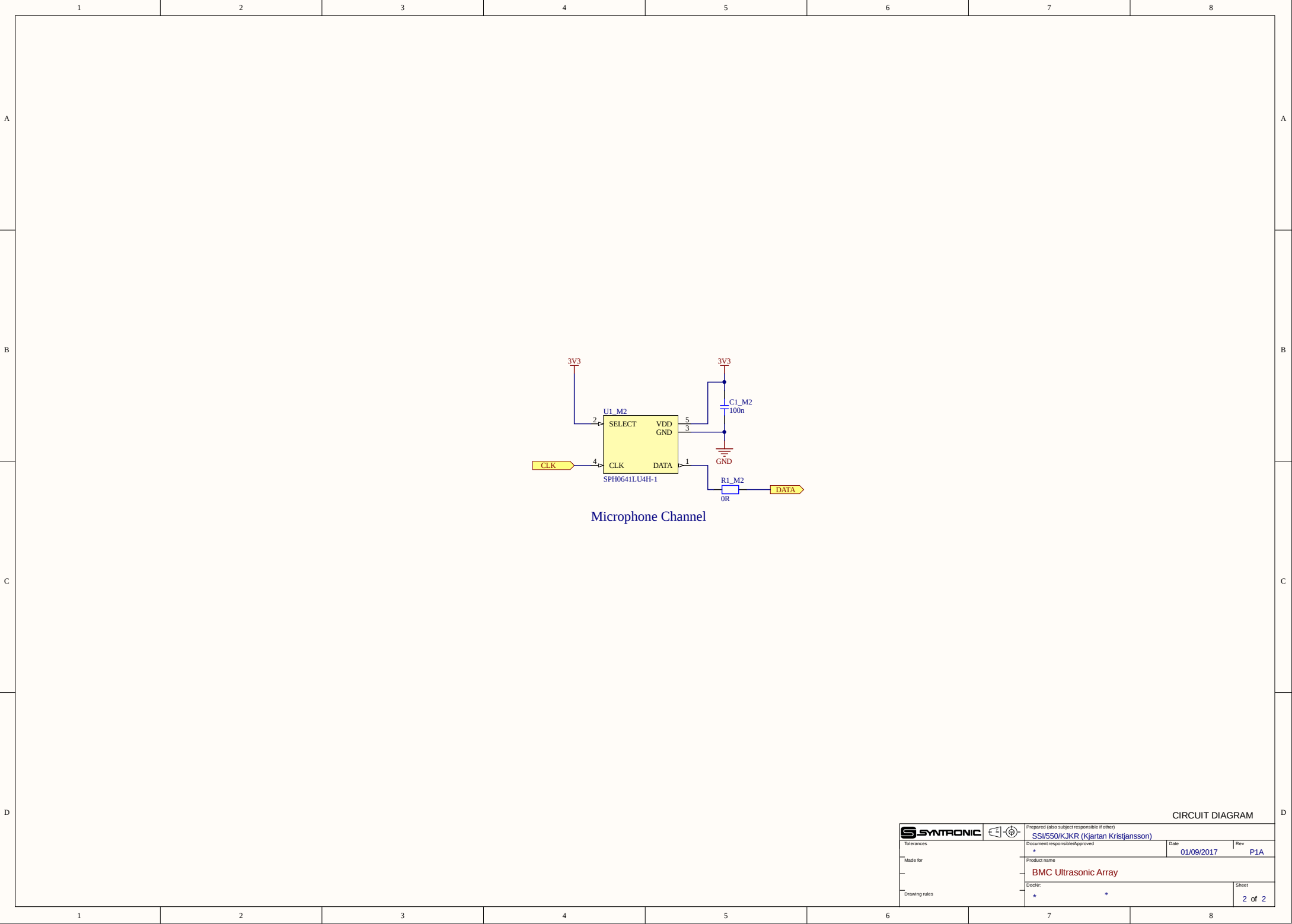
CIRCUIT DIAGRAM			
	Prepared (also subject responsible if other) SSI/550/KJKR (Kjartan Kristjansson)		
	Tolerances	Document responsible/Approved	Rev
	Made for	Date	Rev
	Drawing rules	Product name	Sheet
		BMC Ultrasonic Array	2 of 2



CIRCUIT DIAGRAM			
 	Prepared (also subject responsible if other) SSI/550/KJKR (Kjartan Kristjansson)		
	Tolerances	Document responsible/Approved	Rev
	Made for	Product name	Date
	Drawing rules	Discler:	Sheet
		* *	2 of 2



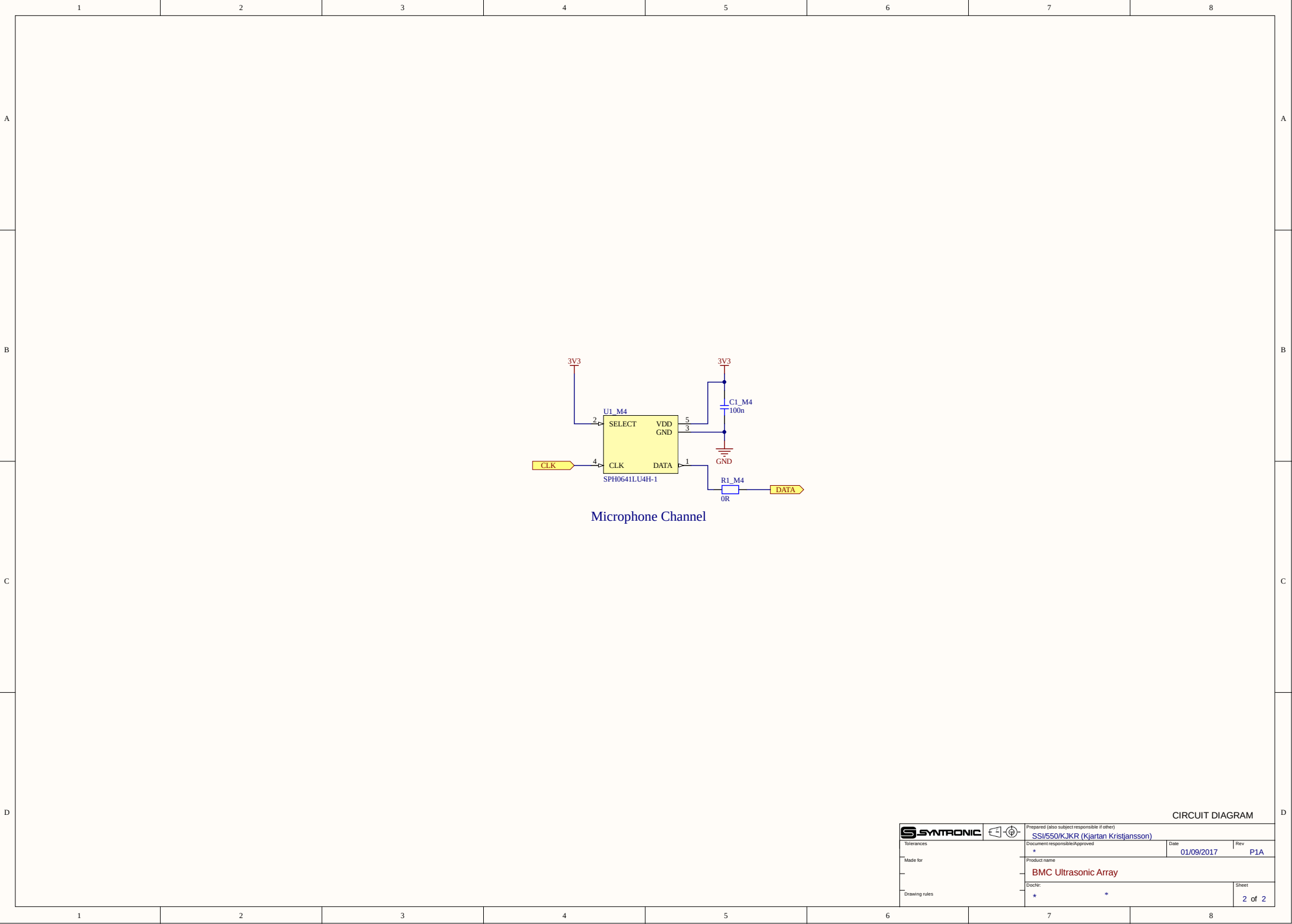
 	Prepared (also subject responsible if other)		
	SSI/550/KJKR (Kjartan Kristjansson)		
Tolerances	Document responsible/Approved	Date	Rev
		01/09/2017	P1A
Made for	Product name		
	BMC Ultrasonic Array		
Drawing rules	Dochte:		Sheet
	* *		2 of 2



CIRCUIT DIAGRAM			
	Prepared (also subject responsible if other)		
	SSI/550/KJKR (Kjartan Kristjansson)		
	Document responsible/Approved		
	Date 01/09/2017 Rev P1A		
Tolerances		Product name	
Made for		BMC Ultrasonic Array	
Drawing rules		Drafter: * *	
		Sheet 2 of 2	



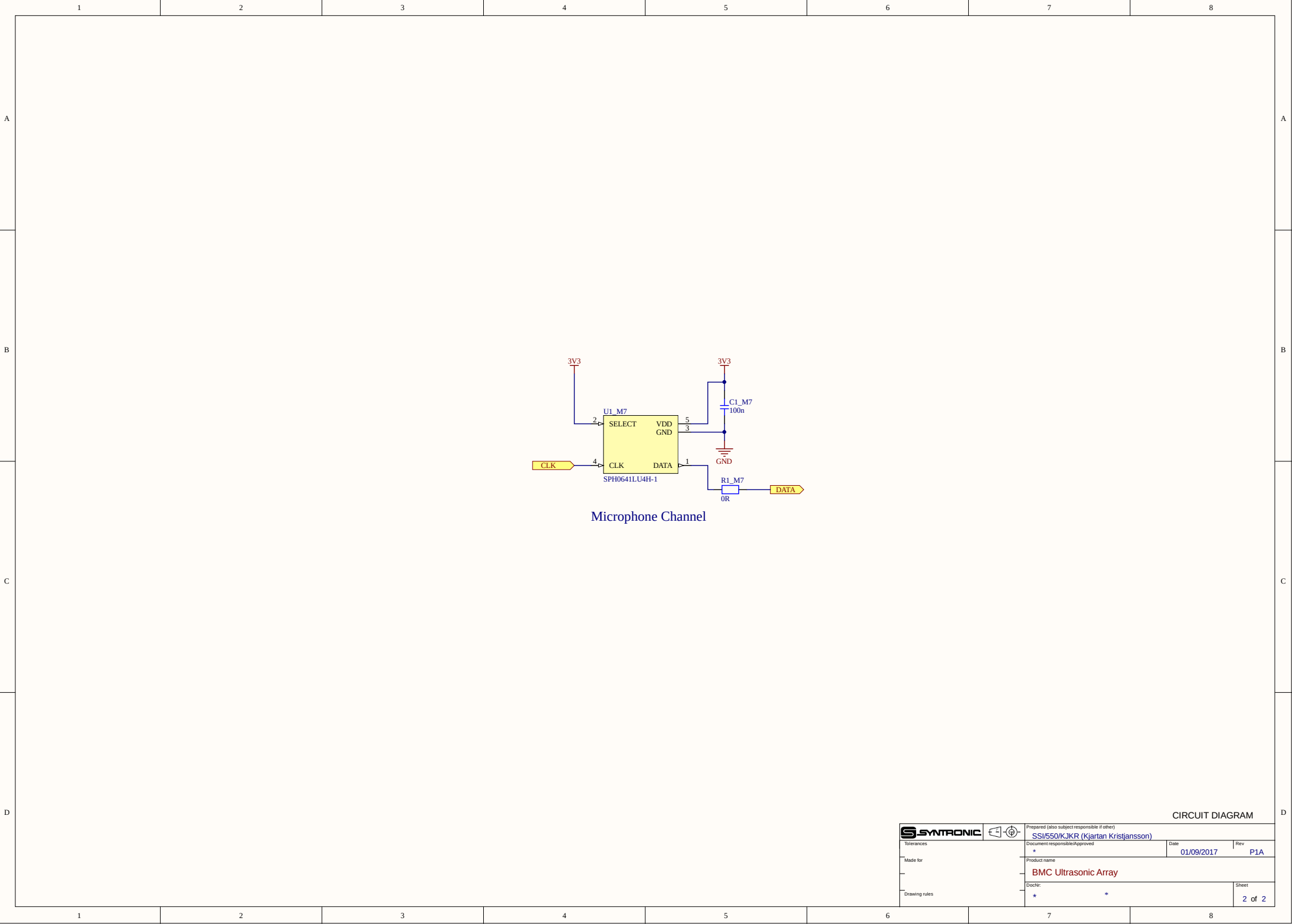
 	Prepared (also subject responsible if other)		
	SSI/550/KJKR (Kjartan Kristjansson)		
Tolerances	Document responsible/Approved	Date	Rev
		01/09/2017	P1A
Made for	Product name		
	BMC Ultrasonic Array		
Drawing rules	Docher:		Sheet
	* *		2 of 2



CIRCUIT DIAGRAM			
	Prepared (also subject responsible if other)		
	SSI/550/KJKR (Kjartan Kristjansson)		
	Document responsible/Approved		
	Date 01/09/2017 Rev P1A		
Tolerances		Product name	
Made for		BMC Ultrasonic Array	
Drawing rules		Drafter: * *	
		Sheet 2 of 2	



 	Prepared (also subject responsible if other)		
	<u>SSI/550/KJKR (Kjartan Kristjansson)</u>		
	Tolerances	Document responsible/Approved	Date
	Made for		01/09/2017
	Product name	P1A	
	BMC Ultrasonic Array		
Drawing rules	Dtache:		Sheet
	★	★	2 of 2



CIRCUIT DIAGRAM				
	Prepared (also subject responsible if other) SSI/550/KJKR (Kjartan Kristjansson)			
	Tolerances	Document responsible/Approved	Date	Rev
	Made for	Product name	01/09/2017	P1A
	Drawing rules	Drachler	Sheet 2 of 2	



 	Prepared (also subject responsible if other)		
	<u>SSI/550/KJKR (Kjartan Kristjansson)</u>		
	Tolerances	Document responsible/Approved	Date
	Made for		01/09/2017
	Product name	P1A	
	BMC Ultrasonic Array		
Drawing rules	Dtache:		Sheet
	★	★	2 of 2



 	Prepared (also subject responsible if other)		
	<u>SSI/550/KJKR (Kjartan Kristjansson)</u>		
	Tolerances	Document responsible/Approved	Date
	Made for		01/09/2017
	Product name	P1A	
	BMC Ultrasonic Array		
Drawing rules	Dtache:		Sheet
	★	★	2 of 2



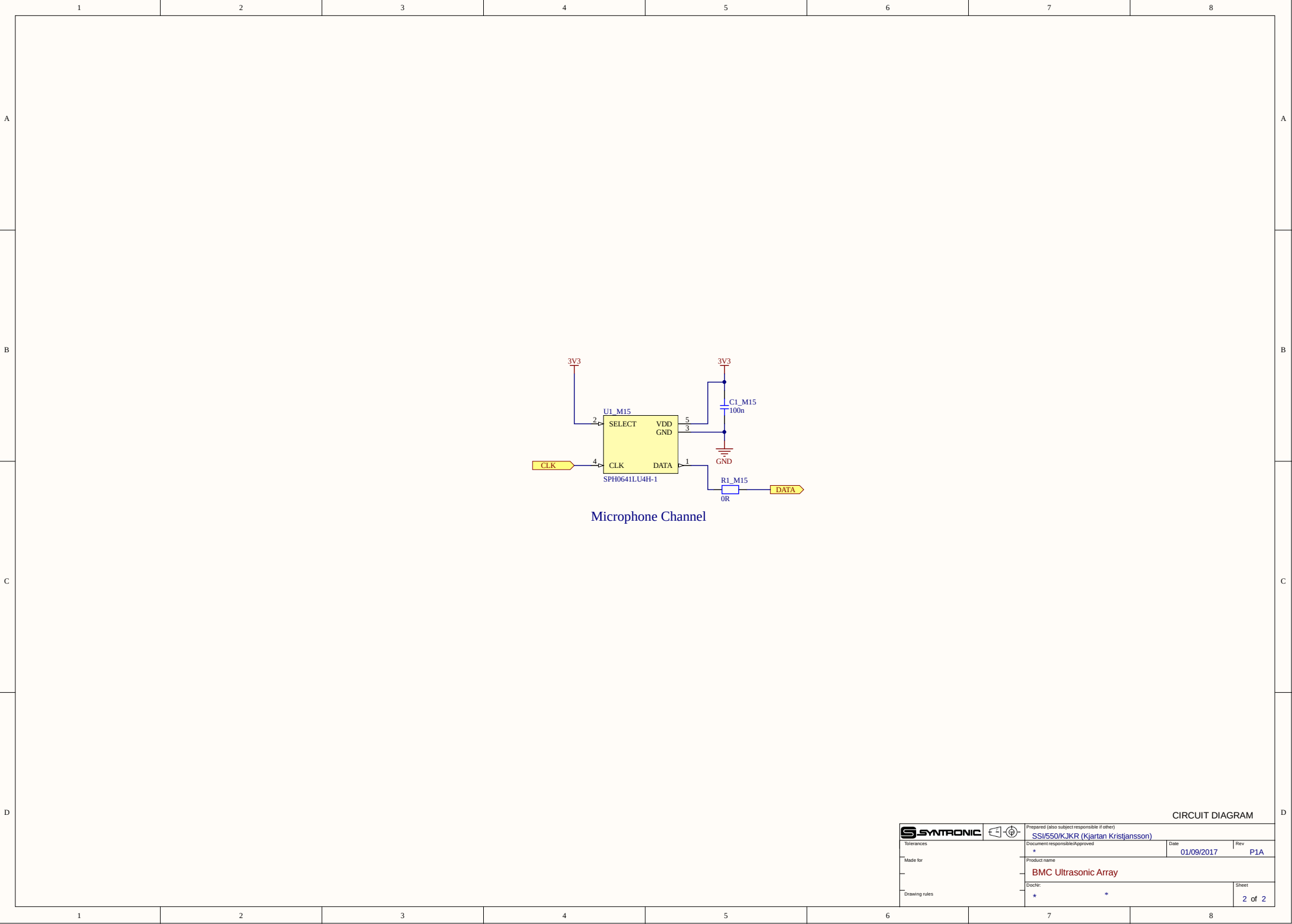
 	Prepared (also subject responsible if other)		
	<u>SSI/550/KJKR (Kjartan Kristjansson)</u>		
	Tolerances	Document responsible/Approved	Date
	Made for		01/09/2017
	Product name	P1A	
	BMC Ultrasonic Array		
Drawing rules	Dtache:		Sheet
	★	★	2 of 2



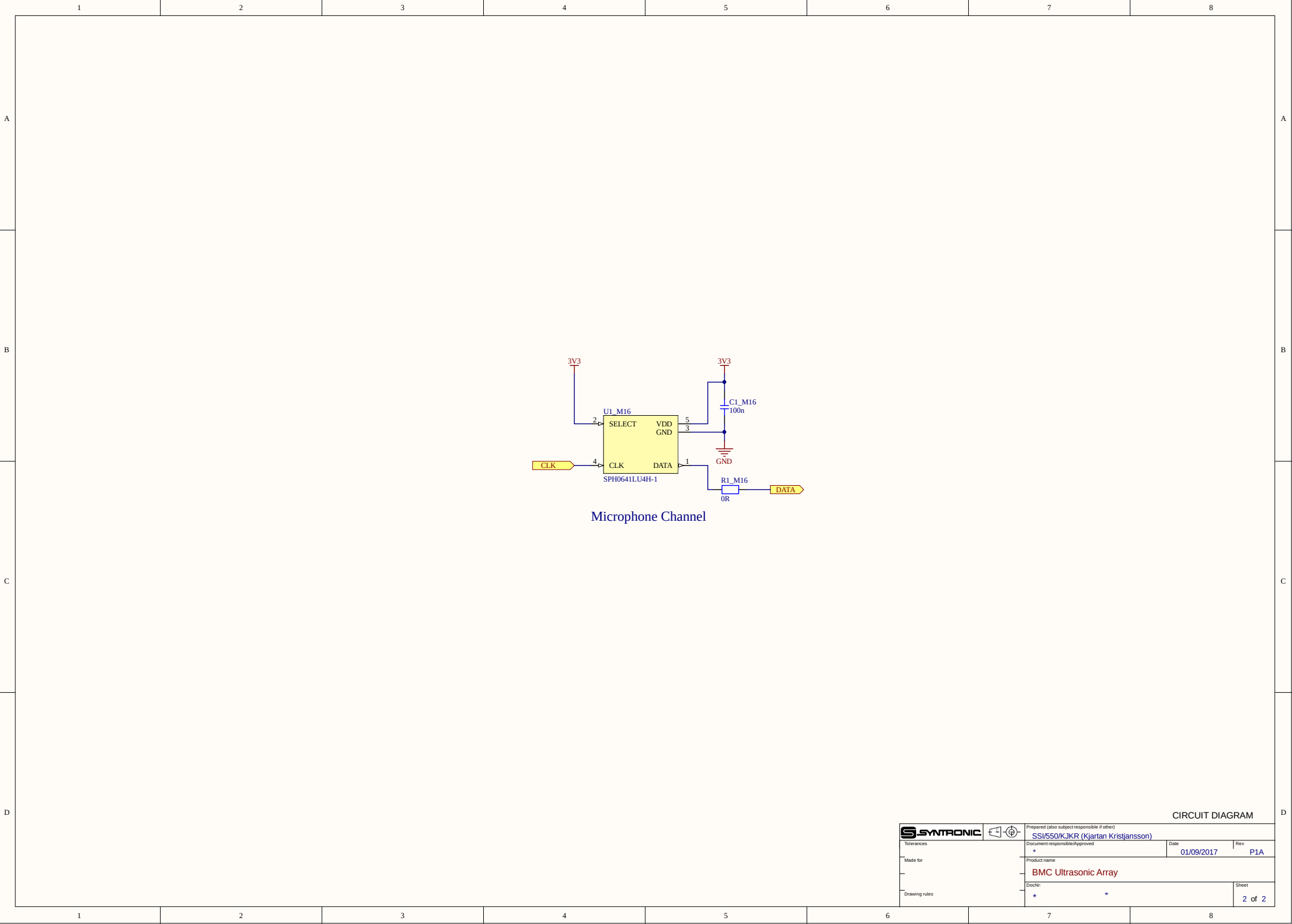
 	Prepared (also subject responsible if other)		
	SSI/550/KJKR (Kjartan Kristjansson)		
Tolerances	Document responsible/Approved	Date	Rev
		01/09/2017	P1A
Made for	Product name		
	BMC Ultrasonic Array		
Drawing rules	Dochte:		Sheet
	* *		2 of 2



 		Prepared (also subject responsible if other) SSI/550/KJKR (Kjartan Kristjansson)	
Tolerances	Document responsible/Approved	Date	Rev
		01/09/2017	P1A
Made for	Product name		
	BMC Ultrasonic Array		
Drawing rules	Ductor:		Sheet
	* *		2 of 2



CIRCUIT DIAGRAM			
	Prepared (also subject responsible if other) SSI/550/KJKR (Kjartan Kristjansson)		
	Tolerances	Document responsible/Approved	Rev
	Made for	Date	Rev
	Drawing rules	Product name	Sheet
		BMC Ultrasonic Array	2 of 2

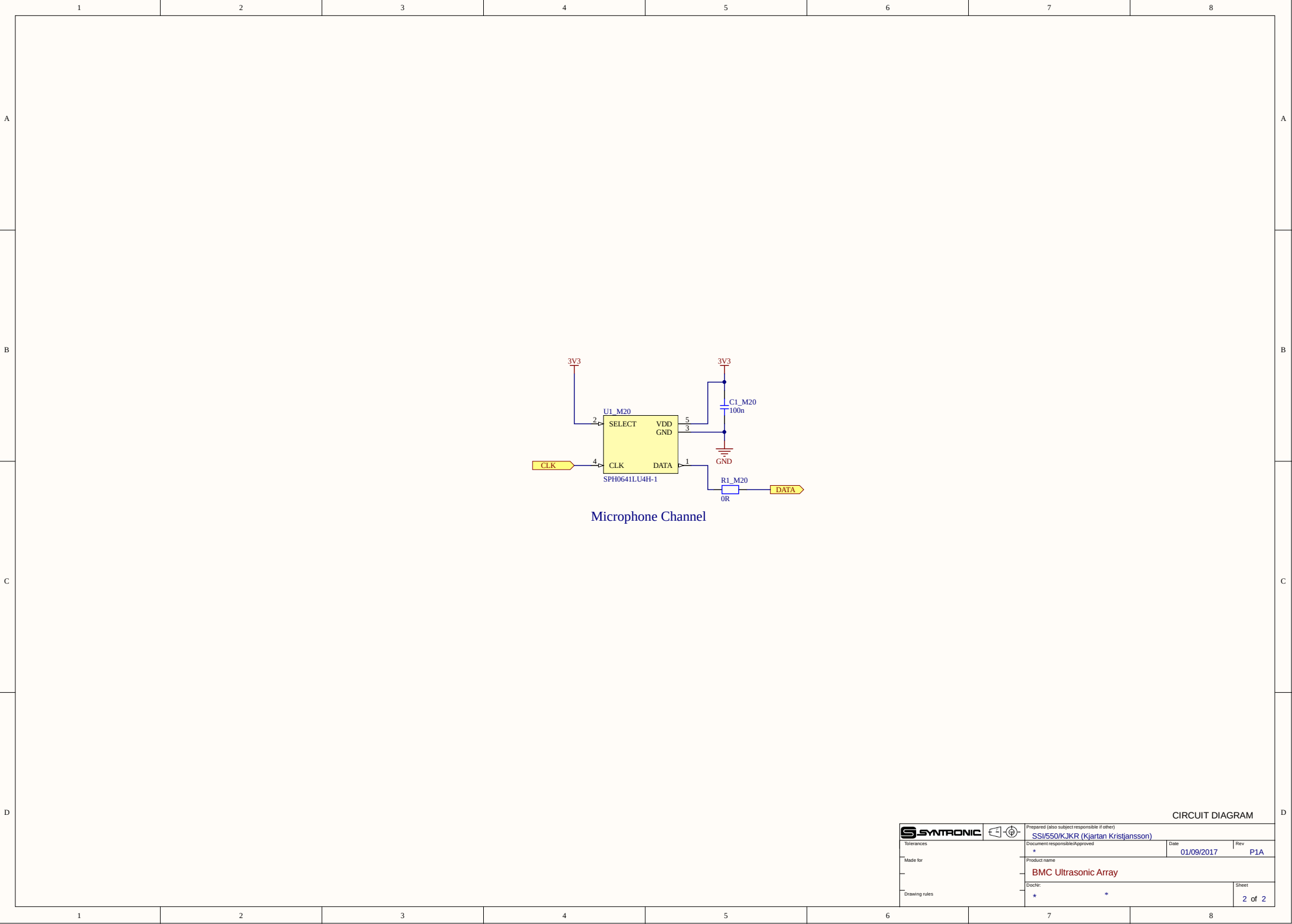


CIRCUIT DIAGRAM				
 	Prepared (also subject responsible if other) SSI/550/KJKR (Kjartan Kristjansson)			
	Document responsible/Approved		Date	Rev
	*		01/09/2017	P1A
	Product name BMC Ultrasonic Array			
Tolerances	Drafter:			Sheet
	* *			2 of 2
Made for				
Drawing rules				





 	Prepared (also subject responsible if other)		
	<u>SSI/550/KJKR (Kjartan Kristjansson)</u>		
	Tolerances	Document responsible/Approved	Date
	Made for		01/09/2017
	Product name	P1A	
	BMC Ultrasonic Array		
Drawing rules	Dtache:		Sheet
	 		2 of 2



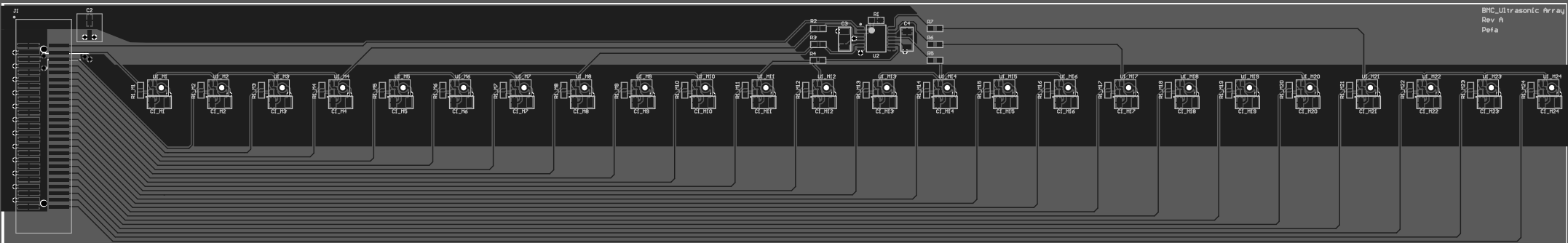
CIRCUIT DIAGRAM			
 	Prepared (also subject responsible if other) SSI/550/KJKR (Kjartan Kristjansson)		
	Tolerances	Document responsible/Approved	Rev
	Made for	Date	Rev
	Drawing rules	Product name	Sheet
		BMC Ultrasonic Array	2 of 2
		01/09/2017	P1A





 		Prepared (also subject responsible if other) <u>SSI/550/KJKR (Kjartan Kristjansson)</u>	
Tolerances	Document responsible/Approved	Date	Rev
		01/09/2017	P1A
Made for	Product name		
	BMC Ultrasonic Array		
Drawing rules	Drafter:	Sheet	
	 	2 of 2	





Comment	Description	Designator	Footprint	LibRef	Quantity
100n	CAP CER 0.1UF 25V C0G/NP0 1206	C1_M1, C1_M2, C1_M3, C1_M4, C1_M5, C1_M6, C1_M7, C1_M8, C1_M9, C1_M10, C1_M11, C1_M12, C1_M13, C1_M14, C1_M15, C1_M16, C1_M17, C1_M18, C1_M19, C1_M20, C1_M21, C1_M22, C1_M23, C1_M24, C3, C4	SMT1206C	CAP100n-1206	26
10u	CAP ALUM 10UF 20% 10V SMD	C2	CAN_RADIAL_B	CAP10u-ALUM	1
SBH41-D25	CONN HEADER 1.27MM 50POS GOLD SMD	J1	SBH41-D25	SBH41-D25_UA	1
10k	RES SMD 10K OHM 1% 1/10W 0603	R1	SMT0603	RES10k-0603	1
OR	RES SMD 0.0OHM JUMPER 1/10W 0603	R1_M1, R1_M2, R1_M3, R1_M4, R1_M5, R1_M6, R1_M7, R1_M8, R1_M9, R1_M10, R1_M11, R1_M12, R1_M13, R1_M14, R1_M15, R1_M16, R1_M17, R1_M18, R1_M19, R1_M20, R1_M21, R1_M22, R1_M23, R1_M24, R2, R3, R4, R5, R6, R7	SMT0603	RES0R_0603	30
SPH0641LU4H-1	MIC MEMS DIGITAL PDM OMNI -26DB	U1_M1, U1_M2, U1_M3, U1_M4, U1_M5, U1_M6, U1_M7, U1_M8, U1_M9, U1_M10, U1_M11, U1_M12, U1_M13, U1_M14, U1_M15, U1_M16, U1_M17, U1_M18, U1_M19, U1_M20, U1_M21, U1_M22, U1_M23, U1_M24	SPH0641LU4H-1	SPH0641LU4H-1	24
PI6C49X0206T	IC CLOCK BUFFER 1:6 14TSSOP	U2	TSOP65P640X120-14N	PI6C49X0206T	1